IDS Doc. Ref. **AP60** for Appl. No. 10/719,058

PULSE-WIDTH MODULATION CONTROL SYSTEM OF INVERTER

Patent number:

JP62012381

Publication date:

1987-01-21

Inventor:

IWASAKI MANABU

Applicant:

FUJI ELECTRIC CO LTD

Classification:

- international:

H02M7/48; H02M7/48; (IPC1-7): H02M7/48

- european:

Application number:

JP19850151737 19850709

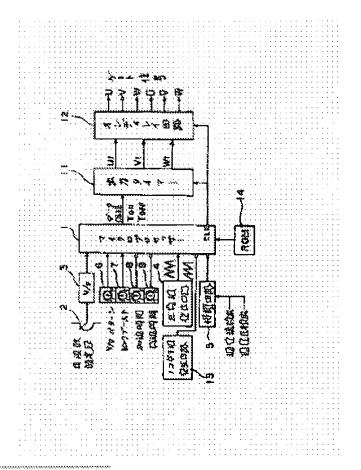
Priority number(s):

JP19850151737 19850709

Report a data error here

Abstract of JP62012381

PURPOSE: To suppress a harmonic wave by switching a pulse-width modulation control wave from a triangular wave to a sawtooth wave when the using frequency of an inverter becomes higher than the prescribed value. CONSTITUTION: A microprocessor 1 inputs a frequency signal through a V/F converter 3 from a frequency setter 2, a modulated triangular wave from a triangular wave generator 4, and a signal from a protecting circuit 5, calculates as prescribed the pulsewidth modulation by a program in a ROM 14, ad produces a gate signal through an output timer 11 to a 3-phase inverter. In this case, a sawtooth wave generator 13 is provided in addition to the generator 4, and a program for controlling to modulate by converting the modulation wave from a triangular wave to a sawtooth wave when the set frequency of the setter 2 becomes higher than the prescribed value is provided in the ROM 14. Thus, the triangular wave is modulated in a low frequency band, and the sawtooth wave is modulated in a high frequency band to suppress harmonic waves.



Data supplied from the esp@cenet database - Worldwide